EE 222 / Introduction to Digital Logic Design

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TYPE	Activity
LEVEL	tertiary
MODALITY	blended,online
SUBJECT	introduction to digital logic design
TECHNOLOGY	internet resource,presentation,whiteboard
KEYWORDS	logic design,digital logic circuit,digital circuit design

Lesson Summary:
After studying logic design concepts from formal lectures, students are required to attend the laboratory to acquire hands-on design skills. Through the laboratory work, students will develop digital logic circuit design skills by conducting software lab exercises which are important for the engineering profession. They will gain problem-solving skills for analyzing digital circuit design problems, synthesizing and evaluating design information.

Lesson Objective:
To provide students with an opportunity to learn and master design and verification of digital logic circuits using open-source software such as Logicly and Logisim.
The interactive Logisim software can be used to get students' attention and also demonstrate the functionality of the logic circuits. The Logicly simulation tool can help students identify and correct logic design errors earlier in the design flow, and reduce hardware design iterations before students build real hardware circuits on breadboards.

MORE INFO	https://www.telmooc.org/